

Module test working group

22/1/2001

# INFN - Pisa

## Module test setup 1

### Lyon system

Basic Test Station (A.Marchioro 25/11/00)

Items in red to be ordered

- PC Dell Optiplex GX110 (linux)
- FEC + PMC-PCI carrier
- CCUM0 (+spare)
- UTRI (+spare)
- FED + PMC-PCI carrier (+spare)
- I/O card CIO6402
- ISA Caenet P.S. controller
- **TSC (PCI)**

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# Module test setup 2

## VME system $\cup$ Mixed system

Items in green have been ordered

Items in red still missing

- VME crate
- RTPC + FED
- TTCvi
- TTCvx
- I2C controller (will be replaced by **FEC**)
- **SBS PCI-VME interface**
- **ESD VME carrier PCI-PMC cards**
  - waiting for a feedback from CERN GROUP
- **Laser Pulser (TTC1x - VME board)**
  - design ready, first draft of **TechnicalDescription and User Manual** written, all components available
  - test of first prototype next month in Torino
  - possible PCI version in the future
  - but: are we going to use a led array for a fast test ?