

Agenda Module test Meeting, 3 June 2003

Introduction

M. Meschini

SW algorithms in ARCS and LT software

T. Affolder

**Cross calibration of TEC modules and
qualification criteria**

V. Zhukov

ARC 6.0, SW algorithms and DB issues

M. Poettgens

Update on the US testing status

A. Bean

Test with backplane pulse and multimodule

with LT 0.91 / 1.0

V. Lemaitre