

# Short Comments on Module Test

Marco Meschini  
INFN Firenze



# On Modules Grading

## What to do when...

- ♣ You find a short: disconnect all but one strip
- ♣ The DCU is giving wrong or “weird” data out:
  - ♦ if the I2C communication is BAD or UNSTABLE, set the module by hand as grade “C” (if not already done automatically by ARC tests),
  - ♦ otherwise set as “B” and write a comment in TComment field in the xml file: the module can be used (with some care). Example: no data from Thermistors



# More on Central TK Database

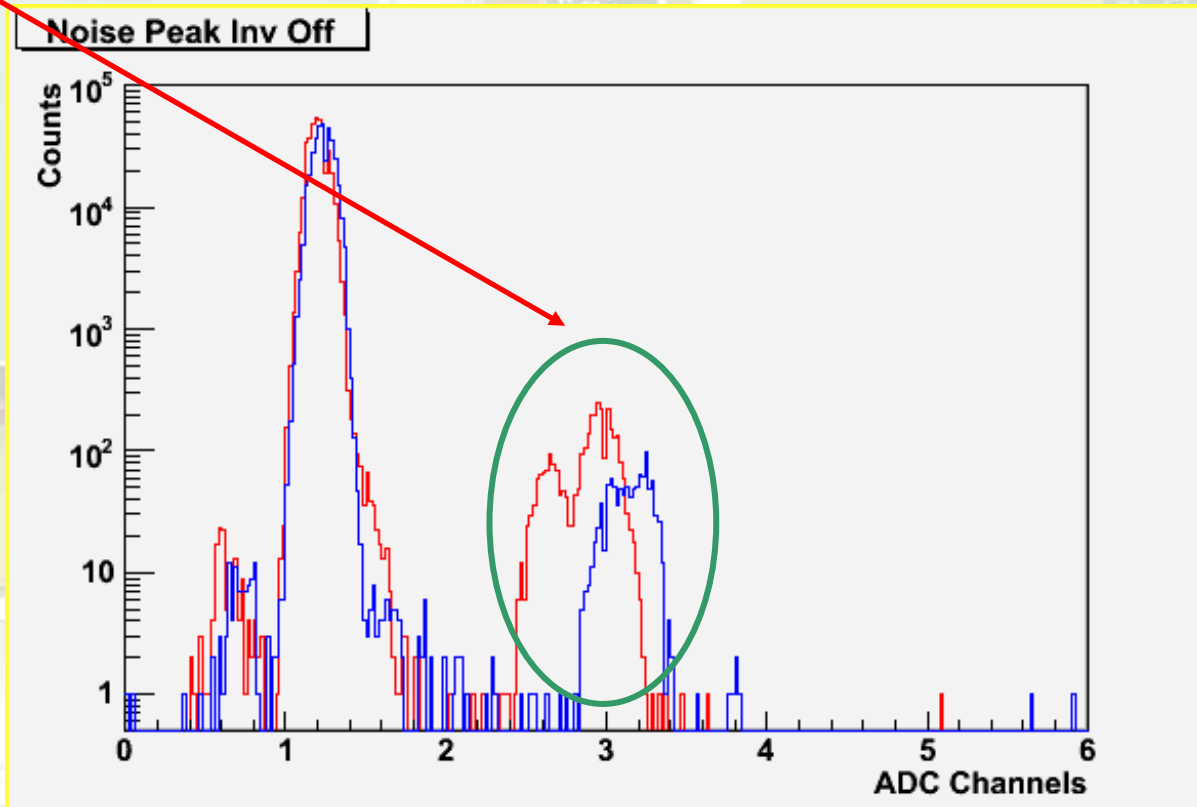
Data quality is crucial: during integration we rely on data from DB!

- ♣ Upload data to DB as soon as you can: modules are not really “done” until everybody can find final results with BigBrowser
- ♣ DO NOT upload “wrong” data, but if you do it, then upload new “corrected” data immediately
- ♣ BE CAREFUL when you perform full tests with FAULTY modules: info in DB **MUST** reflect the real defect and not “derived” ones. Example: modules tested at  $\sim 0$  Volt must not be flagged for “noisy strips”, while it is a  $V_{\text{breakdown}}$  defect



# ARC results from DB: Modules tested with no bias

♣ These data should not go into DB!





# DCU ID

- ♣ Depending on test procedures, sometimes in ARC the DCU ID is not stored in ROOT file
- ♣ This happened in all the TIB centers; maybe TEC and TOB too?
- ♣ The Fast Test should be saved in ROOT file!
- ♣ By the way, the DCU ID can always be found in LT ROOT files
  - ◆ **WARNING: ARC and LT save the DCU ID with reversed byte order!**
- ♣ Example
  - ◆ LT: 5963399 = 0x5AFE87
  - ◆ ARCS (where available): → 0x87FE5A
  - ◆ TIB Integration DB : 8912474 = 0x87FE5A